

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Patent Application of)
YOSHIKAZU KUROSE)
Serial No. (Unassigned))
Filed: April 1, 1999)
For: IMAGE PROCESSING APPARATUS)
AND METHOD OF THE SAME)

jc549 U.S. PTO
09/283231
04/01/99

LETTER TO THE OFFICIAL DRAFTSPERSON

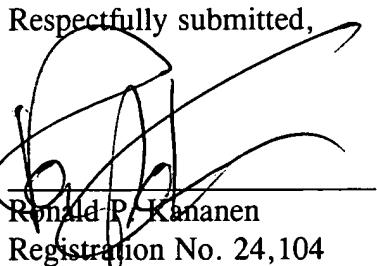
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached please find a copy of the proposed changes to the drawing(s) in this application, for which the approval of the Examiner is requested. Specifically, Figure 4 has been amended to show "Data Flip Flop (160 bit)".

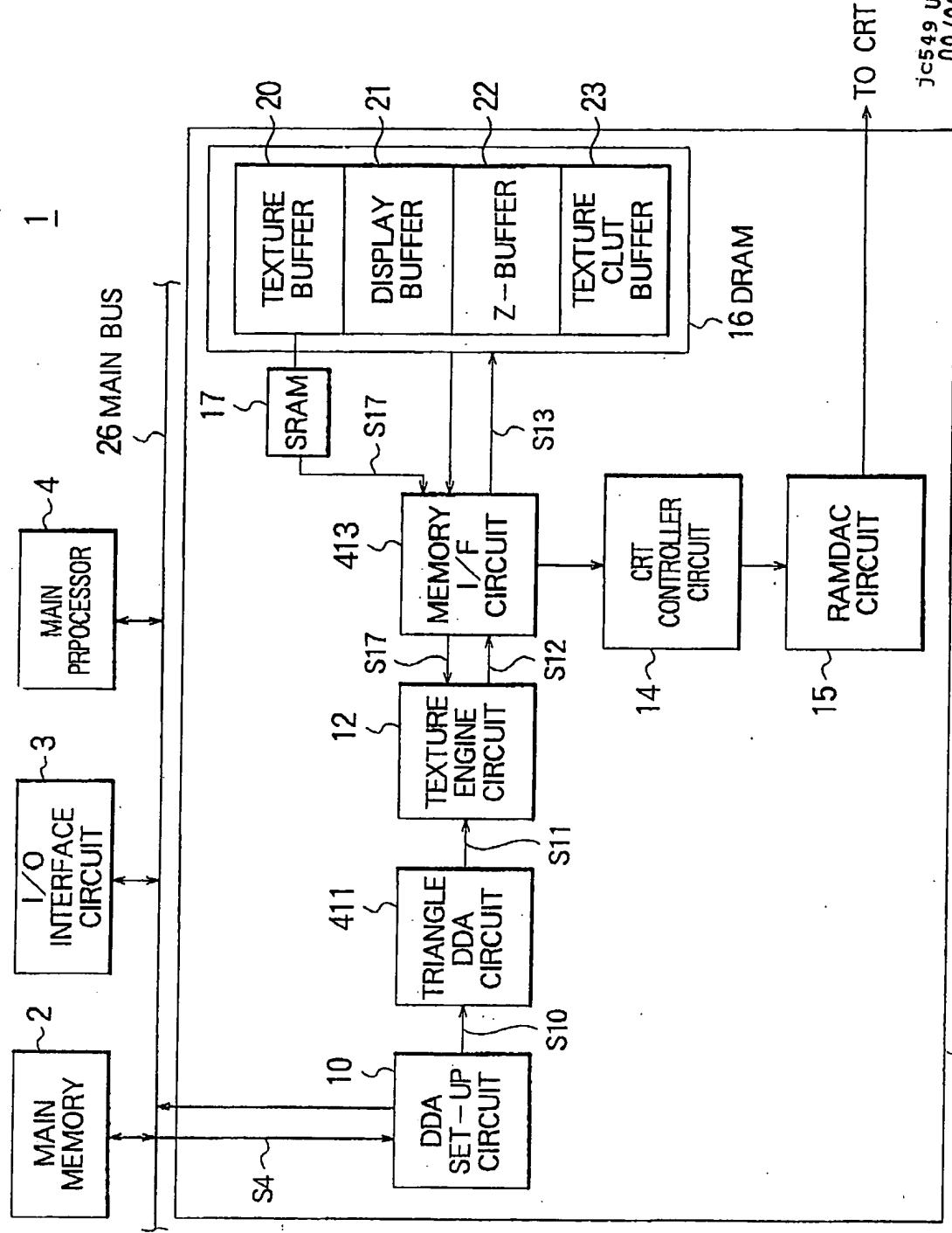
Approved
MR
7/29/01
DATE: April 1, 1999

Respectfully submitted,


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FIG. 1



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5 RENDERING CIRCUIT

04/01/99

FIG. 2

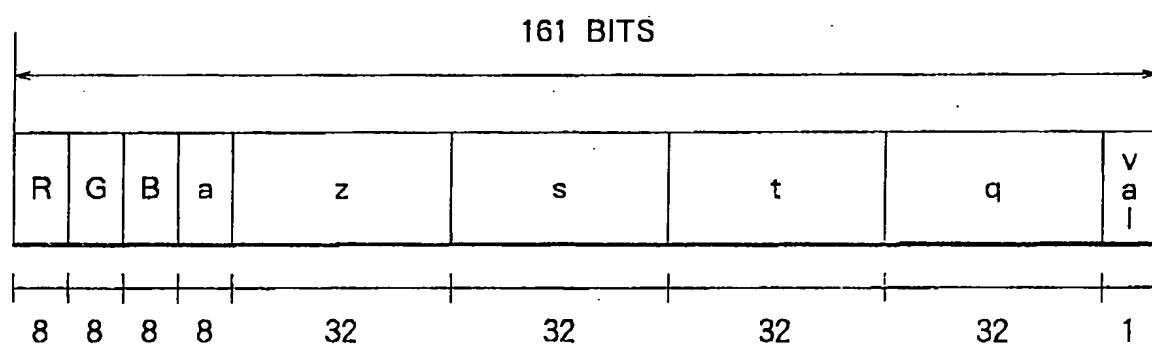


FIG. 3

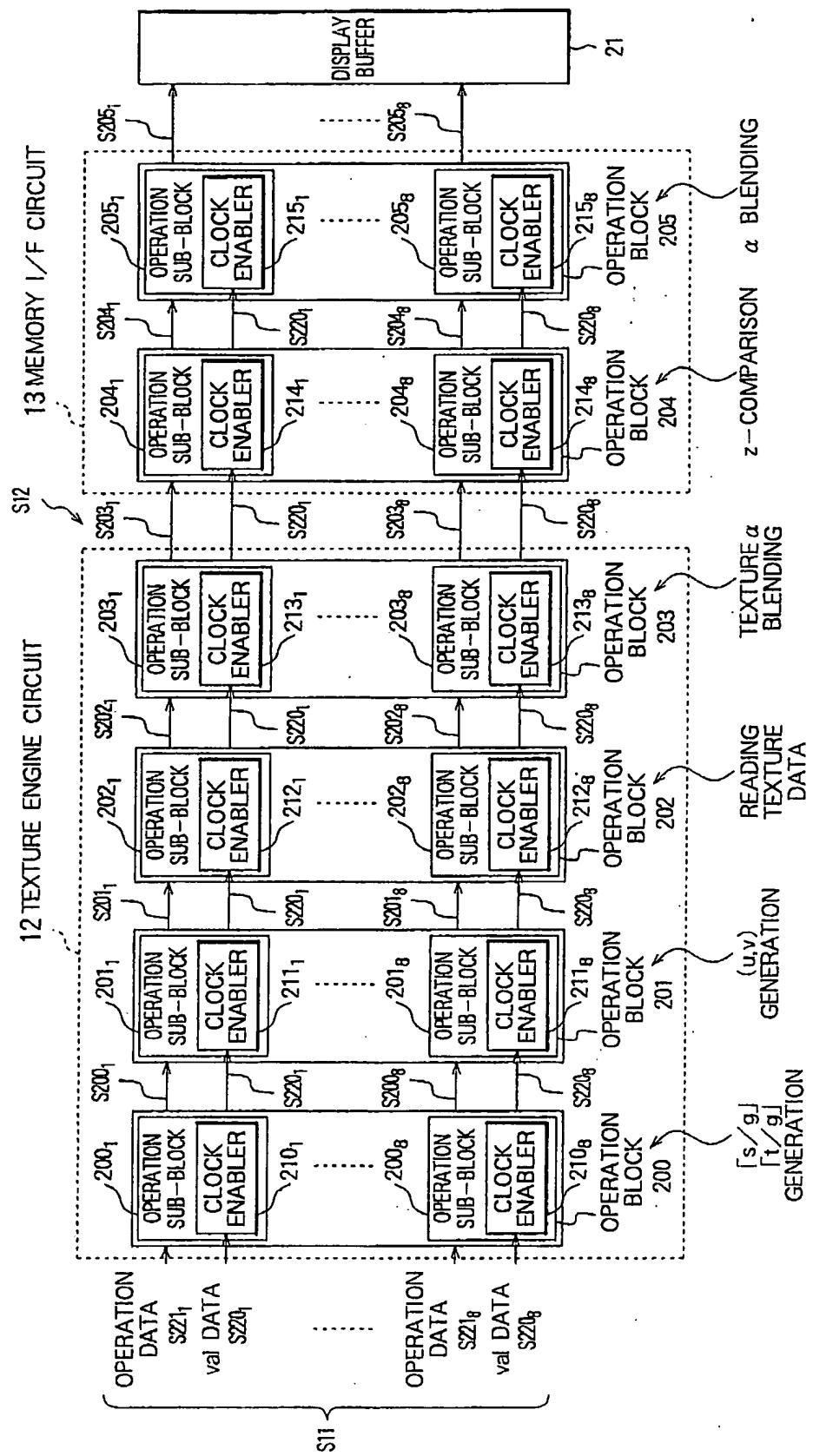


FIG. 4

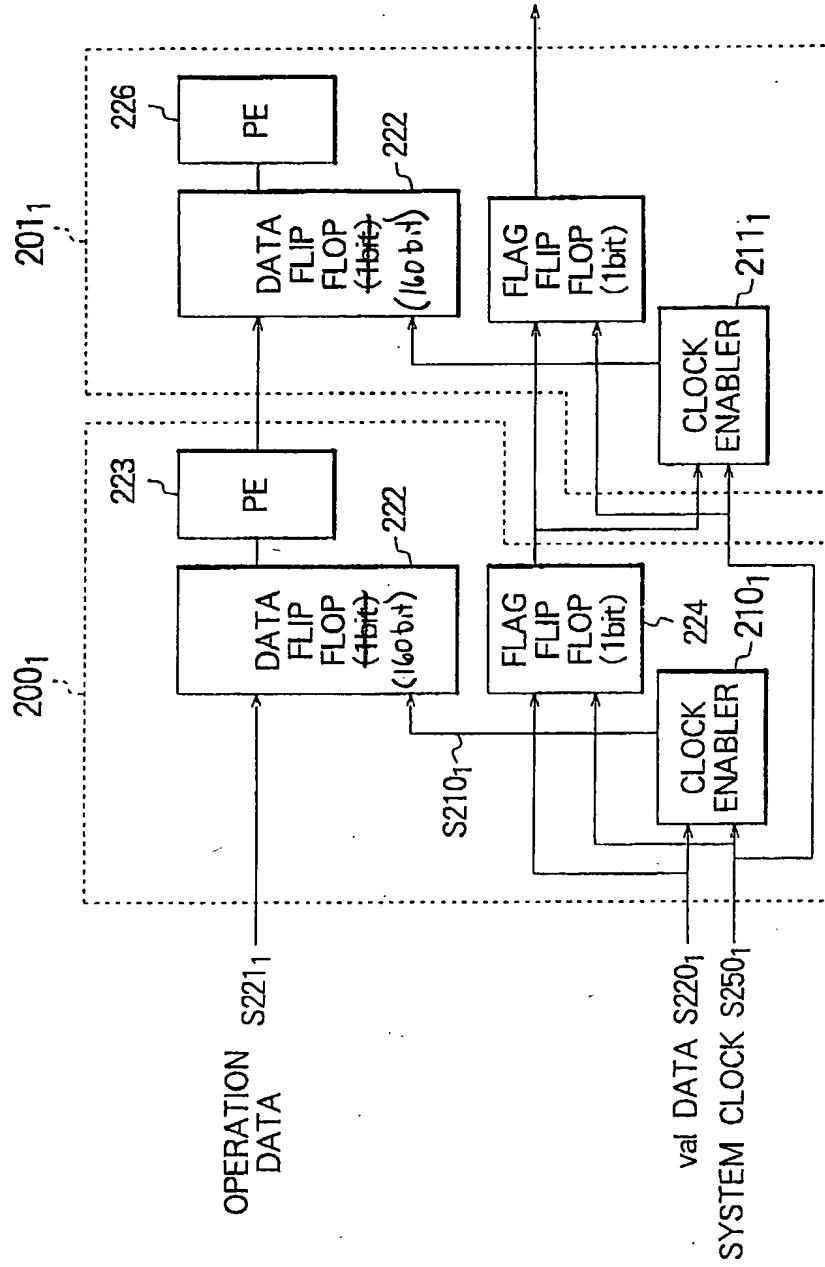
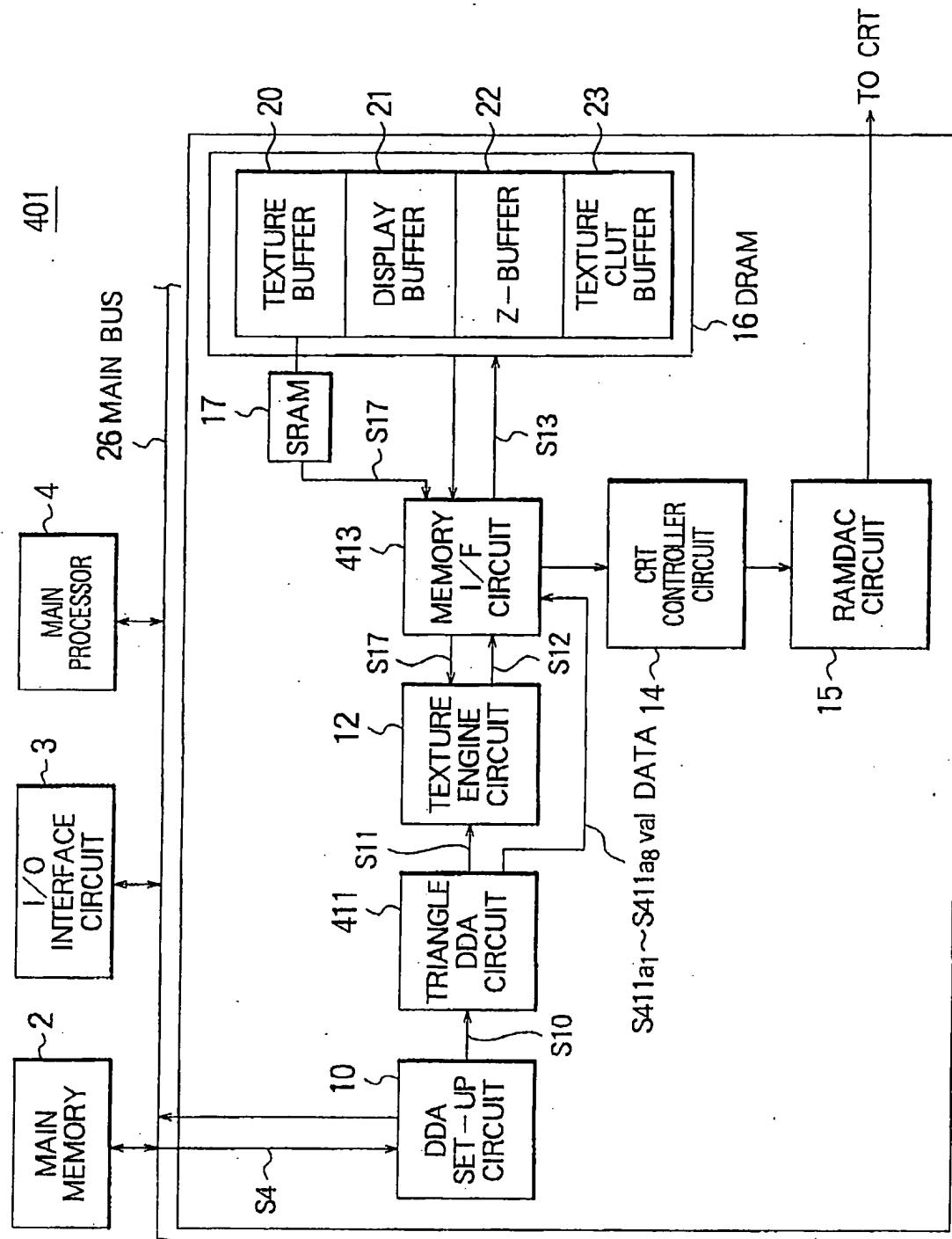


FIG. 5



425 RENDERING CIRCUIT

FIG. 6

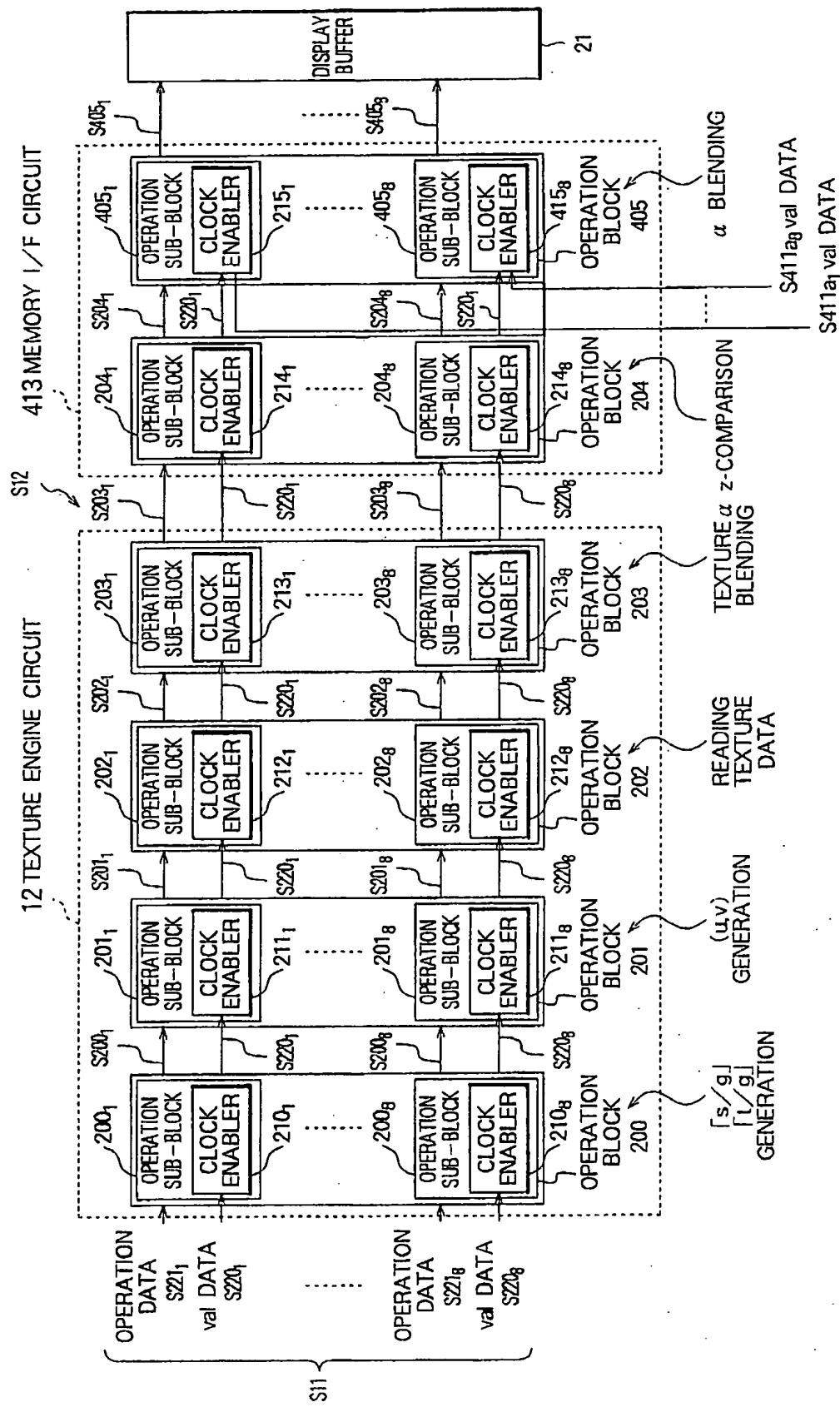


FIG. 7

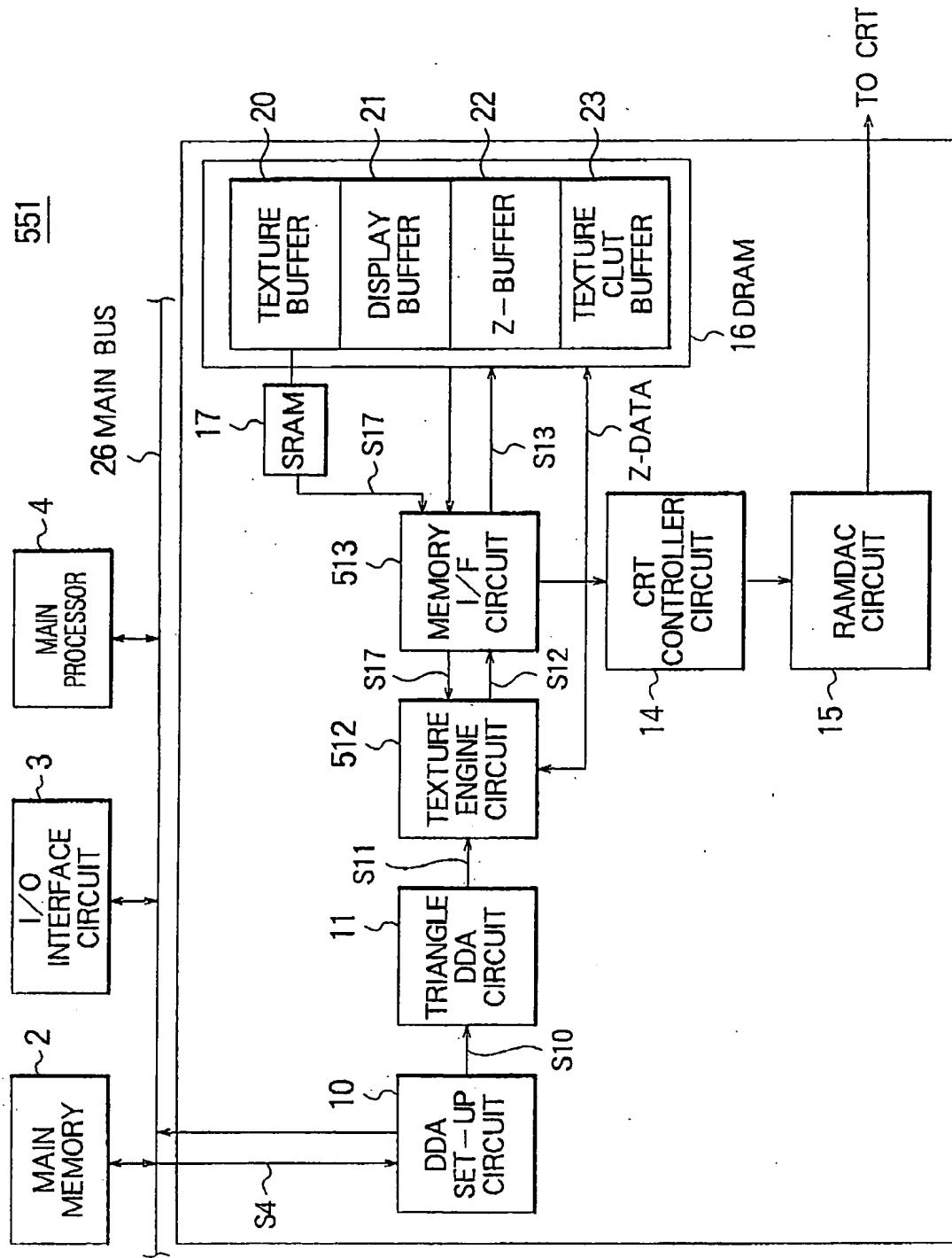


FIG. 8

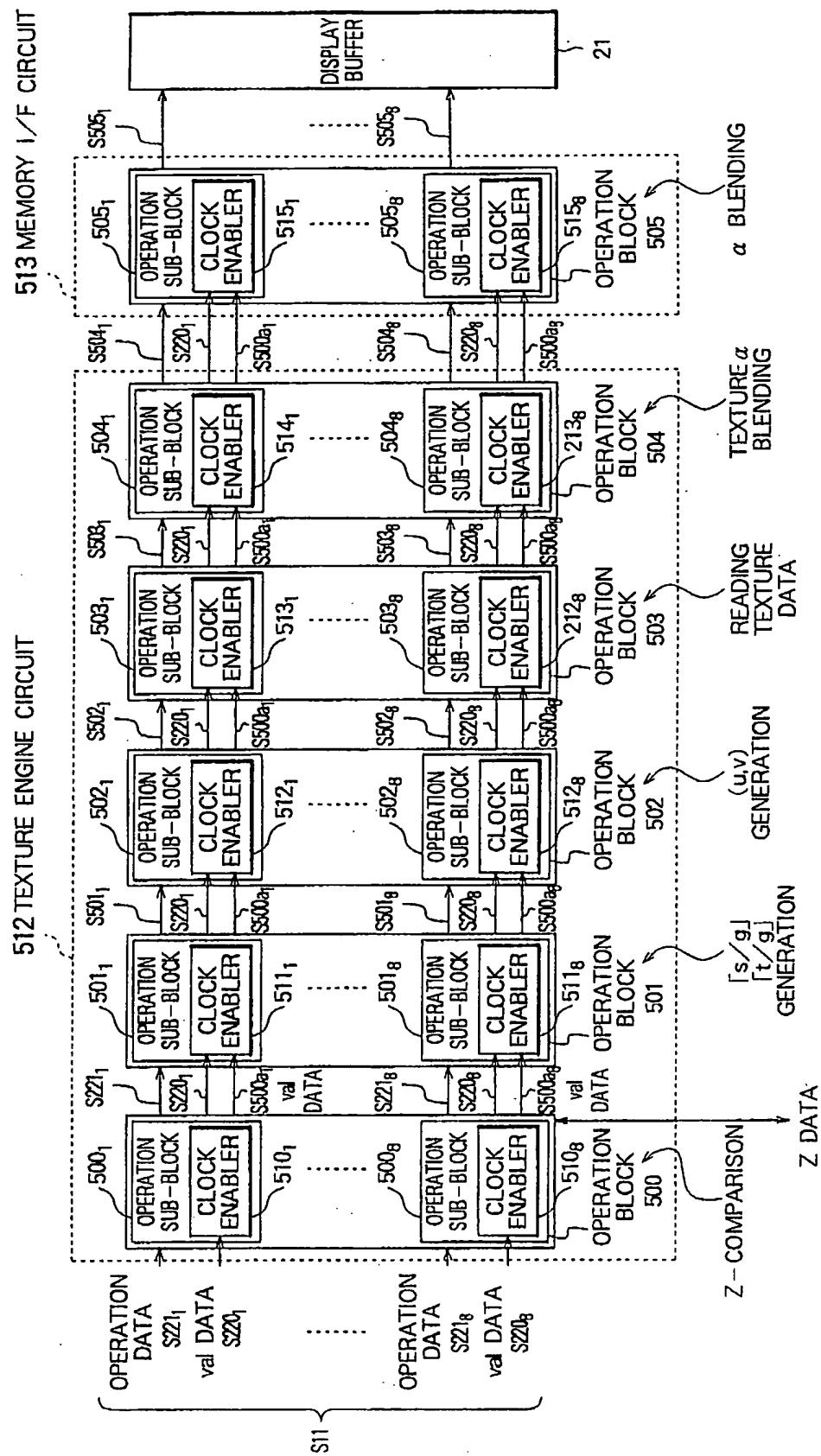


FIG. 9

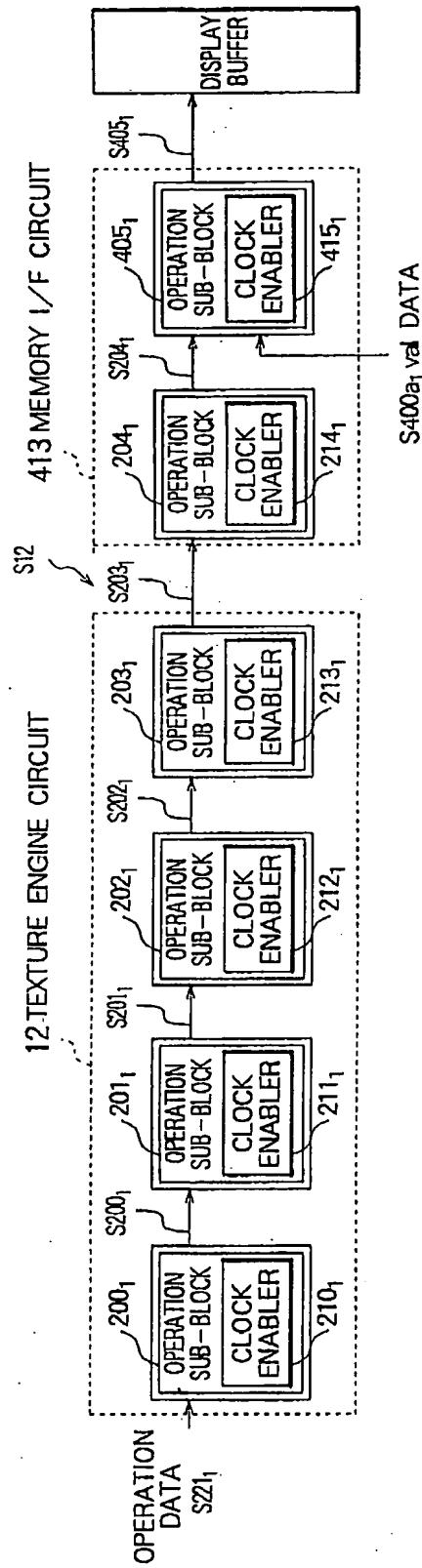


FIG. 10

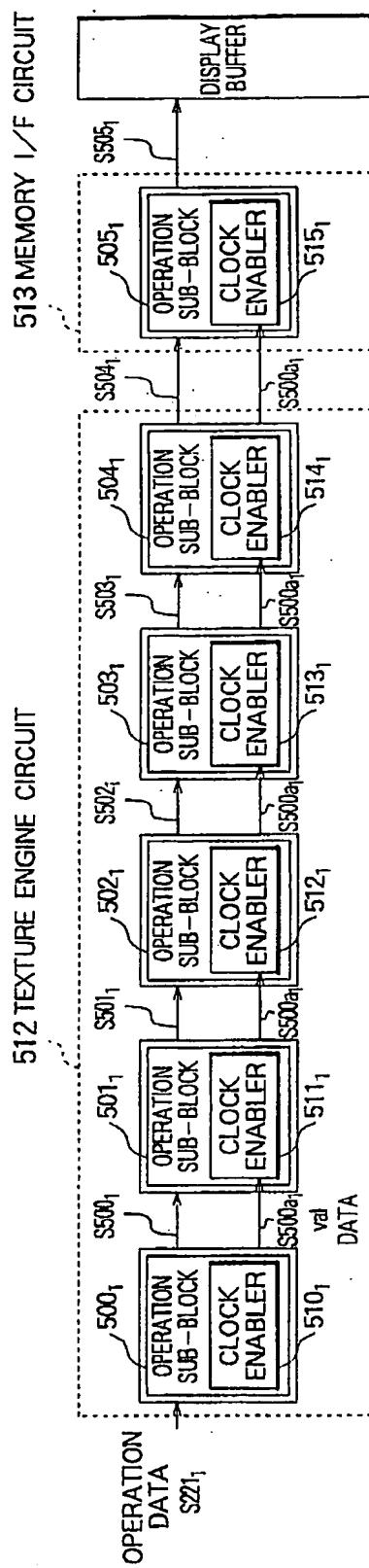


FIG. 11

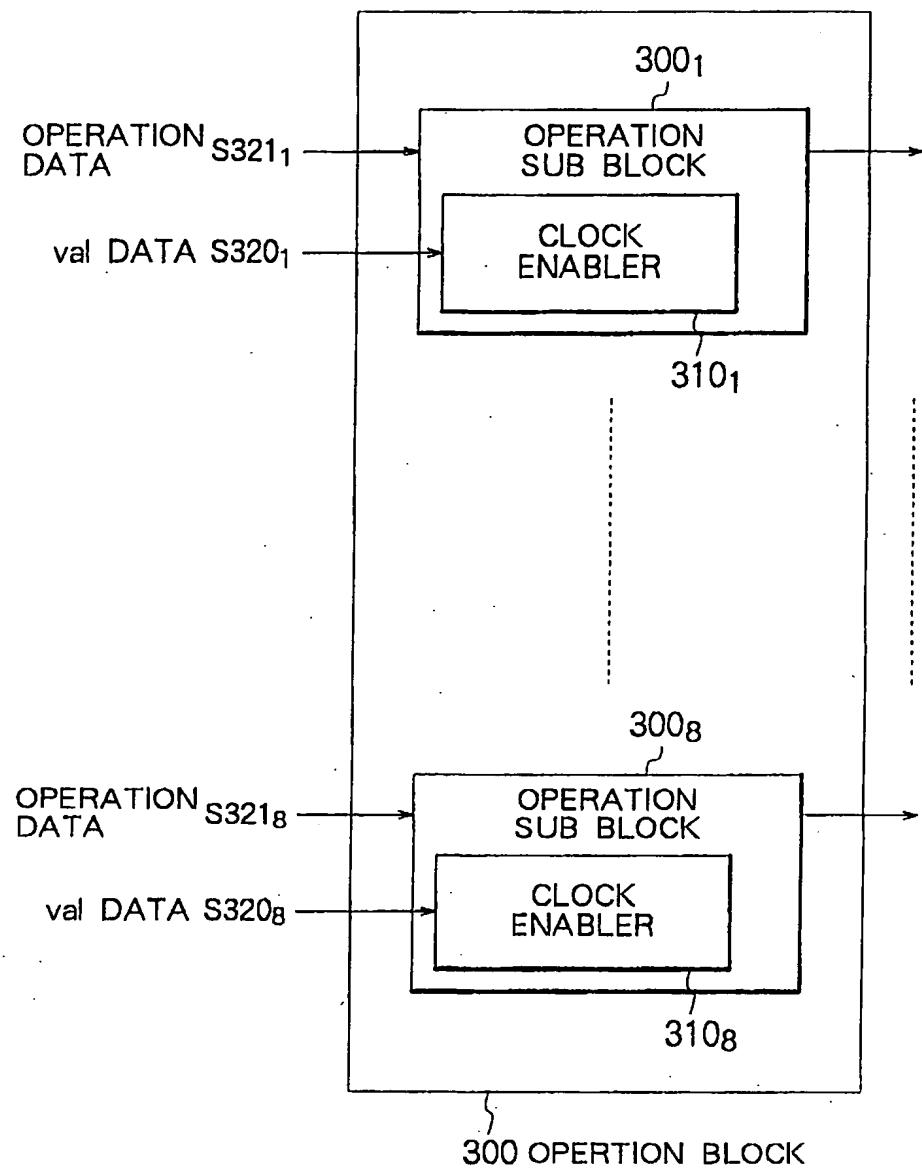


FIG. 12

